

$$- e = -1.6 \times 10^{-19}$$

$$- i = dq/dt \quad - q = it$$

$$- W = V \cdot q$$

$$\text{Power} \Rightarrow I^2 R \text{ or } V^2/R \text{ or } I \cdot V$$

$$- \text{KVL law} \Rightarrow \sum V = 0$$

$$- \text{KCL law} \Rightarrow \sum I_{\text{inside}} = \sum I_{\text{out}}$$

$$- \frac{1}{R_{\text{eq}}} = \frac{1}{R_1} + \frac{1}{R_2} \quad \text{or} \quad \frac{1}{R_{\text{eq}}} = \frac{R_1 + R_2}{R_1 R_2}$$

$$- i = i_1 + i_2 \quad - i = V/R$$

$$- \text{in Nodal Analysis} = I_{\text{higher}} \bar{V}_{\text{higher}} - V_{\text{(Lower)}}/R$$

- for super position we kill ind. sources (1) accept one.

[V \rightarrow short circuit] / [I \rightarrow open circuit]

- How to get thev. theon eqy :-

1. cut the load.

2. kill all the ind. sources.

3. calculate R_{thev} .

4. reconnect the indp. sources.

5. find $V_{\text{thev}} = V_{\text{open circuit}}$.

6. draw thev. eqy ..

- Norton's eq. $\Rightarrow R_N = R_{th}V$.
- If $R_L = R_{th}V$ we reach Max power.
- $P_{max} = V_{th}^2 / 4R_L$ ← power max low.

capacitor's Law :-

- $C = \epsilon_0 A / d$
 - A = Area of the plate
 - d = distance between plate
 - ϵ_0 = permittivity (AIR)
- $q = C \cdot V$
 - $V_C = \frac{1}{C} \int i(t) dt$
- $P = i \cdot V$
 - $W = \frac{1}{2} C V^2$

• in DC-ckt the $i_C / DC = 0$

• capacitor series :-

$$\frac{1}{C_T} = \frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3} \dots$$

$$- q_1 = q_2 = q_3$$

$$\text{or } C_T = C_1 \cdot C_2 / C_1 + C_2$$

$$q_{tot} = C_{eq} \cdot V$$

- Capacitor in parallel :

• $C_T = C_1 + C_2 + C_3 \Rightarrow C_N$

• $V_1 = V_2 = V_3$

• $q_{tot} = q_1 + q_2 + q_3 \dots$

改善

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